

Senior Digital IC Design Engineer

Place: [\[redacted\]](#), The Netherlands

Reference: [\[redacted\]](#)

Company: Our client is one of the key players in the Semiconductor Industry. They focus on three segments: 'Adapter power', 'General Lighting' and 'Display power management'.

Start date: ASAP! So EU citizens or holders of a work permit for The Netherlands only.

Job description: You will design state-of-the-art complex digital IC's for demanding market-driven customers. Tasks require the full spectrum of design skills. Your work will involve design of functional blocks. Tasks include IP creation, verification, logic synthesis, simulation. Familiarity with design for test methodologies is an advantage. You will make use of and drive innovative design methods and tools. You are facilitated by an excellent computer and network infrastructure and CAD services organization. RTL development and functional verification of IP blocks

Requirements: **Mandatory skills:**

- experience in coding with both VHDL and Verilog.
- experience in writing testbenches for IP's.
- experience in independently implementing a specification in RTL
- knowledge of AMBA specification
- experience with synthesis (RTL Compiler Cadence) and simulation (nctools Cadence)

Optional skills:

- spyglass or conformal
- knowledge of CoReUse
- experience in using Specman and Vmanager